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U.S. PATENT DOCUMENTS								
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		6105124	08.15.2000	Farber et al.				
		5418917	05.23.1995	Hiraoka et al.				
		5951678	09.14.1999	Moyer				
		5860152	01.12.1999	Savkar				
		55426742	06.20.1995	Kawasaki et al.				
		5165033	11.17.1992	Kawasaki et al.				
		4654785	03.31.1987	Nishiyama et al.				
		5471593	11.28.1995	Branigin				
		5396603	03.1995	Tamaki et al.				
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		6226716	05.2001	Bauman et al.				
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		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
		2308470	06.25.1997	UK				
OTHER DOCUMENTS <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>								
		"A Comparison of Full and Partial Predicated Execution Support for LLP Processors" by S. Mahlke et al., Proceedings of the Annual Symposium on Computer Architecture, June 22, 1995						
		"Parallelization of Loops with Exits on Pipelined Architectures" by P. Tirumalai et al., Proceedings of the Supercomputer Conference, November 12, 1990						
		"Analysis Techniques for Predicated Code" by R. Johnson et al., Proceedings of the Annual IEEE/ACM Int'l Symposium on Microarchitecture, December 2, 1996						
		"Conditional Annulling for Superscalable Processors" by C.H. Wen et al., Proceedings of the Region Ten Conference (TENCON), October 13, 1993						
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		6202198	03.2001	Bibyk, Steven B.			
		6075935	06.2000	Ussery et al.			
		5539888	07.1996	Byers et al.			
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		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
							YES
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